

ABSTRACT OF THE DISCLOSURE

A liquid crystal display includes pixels arrayed in a matrix of rows and columns, scanning lines extending along the rows of the pixels, signal lines 5 extending along the columns of the pixels, and pixel driving sections which are disposed near intersections of the scanning lines and signal lines, and each of which is controlled via one scanning line to capture a data signal on one signal line and output the data 10 signal to one pixel. Particularly, each pixel driving section includes a memory circuit having a transistor whose gate is connected to the one signal line, and first and second storage capacitances which are charged to positive and negative power supply voltages and 15 connected to a source and drain of the transistor to store the data signal as analog drive voltages of positive and negative polarities, respectively.